

SPS-2125PFW-CXX0G

(RoHS Compliant)

64GFC / 25 km / CWDM Digital Diagnostic SFP56 LC SINGLE-MODE TRANSCEIVER

**FEATURES**

- Up to 57.8 Gbps Bi-directional Data Links
- Duplex LC Connector
- Compatible with Fibre Channel 64GFC-LW (PAM4)
- Support Fibre Channel 32GFC and 16GFC data rates (NRZ)
- Distance up to 25 km SMF
- Cooled CWDM EML Transmitter: from 1270 nm to 1330 nm
- SFF-8472 Digital Diagnostic Function
- Rx Output Amplitude Control Implemented
- AC/AC Coupling according to MSA
- Power Consumption < 3.0W
- Single +3.3 V Power Supply
- RoHS Compliant
- 0 to 70°C Operation
- Class 1 Laser International Safety Standard IEC-60825 Compliant

**APPLICATIONS**

- Fibre Channel 64GFC-LW
- Fibre Channel 32GFC and 16GFC

**DESCRIPTION**

The SPS-2125PFW-CXX0G series single mode transceiver is a small form factor pluggable module for bi-directional serial optical data communications such as Fibre Channel 64GFC-LW. It is with the SFP+ 20-pin connector to allow hot plug capability. Digital diagnostic functions are available via an I<sup>2</sup>C. This module is designed for single mode fiber and operates at a nominal wavelength of CWDM. The transmitter section consists of a cooled CWDM EML and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a rate selection clock data recovery (CDR) IC.

**LASER SAFETY**

This single mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

**ORDER INFORMATION**

P/No.	Bit Rate (Gb/s)	FC	Modulation Format	Distance (km)	Wavelength (nm)	Package	Case Temp (°C)	RoHS Compliant
SPS-2125PFW-CXX0G	Up to 57.8	64G/32G/16G	PAM4/NRZ	25	CWDM*	SFP56 with DMI	0 to 70	Yes

CWDM\* Wavelength

Central Wavelength	Min. (nm)	Typ. (nm)	Max. (nm)	Label	Central Wavelength	Min. (nm)	Typ. (nm)	Max. (nm)	Label
-C270	1264.5	1270	1277.5	C27	-C310	1304.5	1310	1317.5	C31
-C290	1284.5	1290	1297.5	C29	-C330	1324.5	1330	1337.5	C33

CWDM\*: 4 wavelengths from 1270 nm to 1330 nm, each step 20 nm.

Absolute Maximum Ratings					
Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Relative Humidity	RH	5	85	%	Non-condensing
Operating Case Temperature	Topr	0	70	°C	

Recommended Operating Conditions					
Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.135	3.3	3.465	V
Power Dissipation	P <sub>D</sub>			3.0	W
Operating Case Temperature	Topr	0		70	°C
Data Rate		14.025		28.9	GBd
		14.025		57.8	Gb/s

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Transmitter Optical Specifications (Topr= 0 to 70°C, Vcc3 = 3.3V ±5%)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Output Center Wavelength	$\lambda$	$\lambda_c - 5.5$	$\lambda_c$	$\lambda_c + 7.5$	nm	1
Side Mode Suppression Ratio	SMSR	30			dB	
Average Launch Power	$P_{o, Avg}$	0		5.6	dBm	2
Outer OMA	$OMA_{outer}$	3.0		6.4	dBm	
Launch power in $OMA_{outer}$ minus TDECQ		1.6			dBm	
Transmitter and dispersion penalty eye closure for PAM4	TDECQ			4	dB	3
TDECQ- $10\log_{10}(C_{eq})$				4	dB	
Average Launch Power of OFF Transmitter				-16	dBm	
Extinction Ratio	ER	3.5			dB	
Optical return loss tolerance				15.6	dB	
Transmitter reflectance				-26	dB	4
Transmitter transition time				34	ps	20% - 80%
$RIN_xOMA$				-134	dB/Hz	

- ITU-T G.694.2 CWDM wavelength from 1270 nm to 1330 nm, each step 20 nm.
- The value is calculated using an infinite extinction ratio at the lowest allowed transmit  $OMA_{outer}$ . A launched power below this value cannot be compliant; however, a value above this does not ensure interoperation with compliant receivers.
- For FC-PI-7, TDECQ must be computed using a SER of  $2.18 \times 10^{-4}$ .
- Transmitter reflectance is defined looking into the transmitter.

Receiver Optical Specifications (Topr= 0 to 70°C, Vcc3 = 3.3V ±5%)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Wavelength of Operation		1260		1360		
Damage Threshold		-2.4			dBm	5
Average receive power		-14.7		-3.4	dBm	
Receiver power (OMA)	$OMA_{outer}$			-2.6	dBm	
Receiver reflectance				-26	dB	
Receiver sensitivity ( $OMA_{outer}$ )@ 64GFC	Sen 1			RS	dBm	RS= max (-12.5, TECQ-13.9) @ 28.9GBd, BER = 1.09E-4
Receiver sensitivity (OMA)@ 32GFC	Sen 2			TBD	dBm	@ 28.05Gbps, BER = 1E-6
Receiver sensitivity (OMA)@ 16GFC	Sen 3			TBD	dBm	@ 14.025Gbps, BER = 1E-12
LOS Assert	Pa	-35			dBm	6
LOS Deassert	Pd			-14.7	dBm	6
LOS Hysteresis		0.5		4	dB	

- The receiver shall be able to tolerate, without damage, continuous exposure to an optical signal having this average power level. The receiver does not have to operate correctly at this input power.
- Rx LOS pin output is controlled by software; Rx LOS assert/deassert delay time is < 100ms.

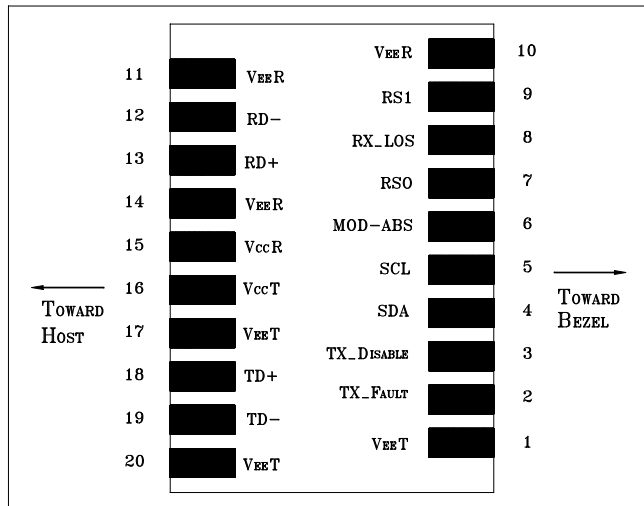
Electrical Characteristics						
Parameter	Symbol	Min	Typ	Max	Units	Notes
<b>High-Speed Signal (CML) Interface Specification</b>						
64GFC Singling Rate			28.9		GBd	+/- 100 ppm PAM4
32GFC Singling Rate			28.05		GBd	+/- 100 ppm NRZ
16GFC Singling Rate			14.025		GBd	+/- 100 ppm NRZ
Differential Input Impedance	$R_{in}$	90	100	110	$\Omega$	
Differential Data Input Amplitude		900			mVpp	Internally AC coupled
Output Singling Rate		14.025		28.9	GBd	
Differential Output Impedance	$R_{out}$	90	100	110	$\Omega$	
Differential Data Output Amplitude				900	mVpp	Internally AC coupled

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Low-Speed Signal (LVTTL) Interface Specification					
Input High Voltage		2.0		V <sub>cc</sub> +0.3	V
Input Low Voltage		GND		0.8	V
Output High Voltage		2.4		V <sub>cc</sub>	V
Output Low Voltage		GND		0.5	V

## CONNECTION DIAGRAM



PIN	Signal Name	Description	PIN	Signal Name	Description
1	VEE T	Transmitter Signal Ground	11	VEE R	Receiver Signal Ground
2	TX_Fault	Transmitter Fault Indication. Logic “1” Output = Laser Fault. Logic “0” Output = Normal Operation	12	RD-	Inverse Receiver Data Out
3	TX_Disable	Logic “1” Input (or no connection) = Laser off, Logic “0” = Laser on.	13	RD+	Receiver Data Out
4	SDA	Modulation Definition 2 – Two wires serial ID Interface	14	VEE R	Receiver Signal Ground
5	SCL	Modulation Definition 1 – Two wires serial ID Interface	15	VCC R	Receiver Power – 3.3V±5%
6	MOD-ABS	Modulation Definition 0 – Ground in Module	16	VCC T	Transmitter Power – 3.3V±5%
7	RS0	RX Rate Select: See Rate Select Control This pin has an internal 30k pulldown to ground.	17	VEE T	Transmitter Signal Ground
8	RX_LOS	Loss of Signal Out (OC).	18	TD+	Transmitter Data In
9	RS1	TX Rate Select: See Rate Select Control This pin has an internal 30k pulldown to ground.	19	TD-	Inverse Transmitter Data In
10	VEE R	Receiver Signal Ground	20	VEE T	Transmitter Signal Ground

## MODULE DEFINITION

Module Definition	PIN 4	PIN 5	PIN 6	Interpretation by Host
4	SDA	SCL	MOD-ABS	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, SDA and SCL appear as no connector (NC) and MOD-ABS is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E<sup>2</sup>PROM protocol of the ATMEL AT24C01A/02/04 family of components.

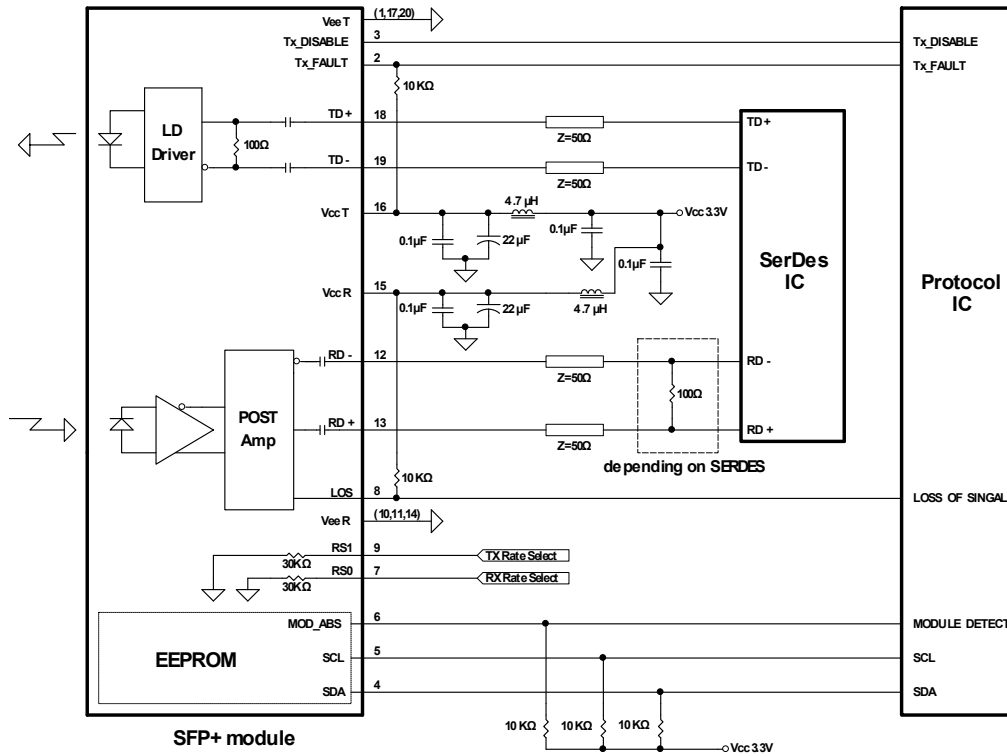
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**RATE SELECT CONTROL**

1. TX & RX rates and modes can be controlled either by the hardware input pins or the register control bits.
2. TX & RX only operate at the same speed and mode.
3. TX & RX cannot be independently controlled.

TX & RX		Rate Select Control Inputs		
Rate	Mode	Software 64GFC Mode  Software A2h Byte 119 Bit 2	RS0  Hardware Pin 7 or Software A2h Byte 110 Bit 3	RS1  Hardware Pin 9 or Software A2h Byte 118 Bit 3
16GFC	NRZ	0	0	0
32GFC	NRZ	0	0	1
32GFC	NRZ	0	1	0
32GFC	NRZ	0	1	1
64GFC	PAM4	1	X	X

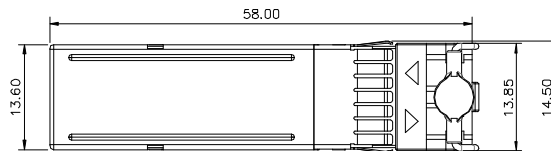
## RECOMMENDED CIRCUIT SCHEMATIC



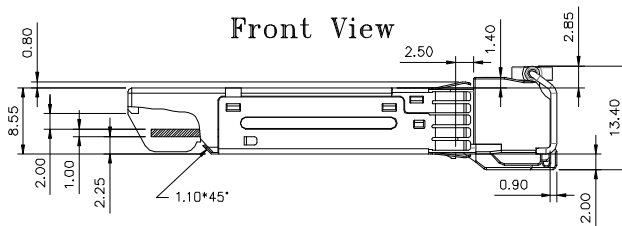
## PACKAGE DIAGRAM

Units in mm

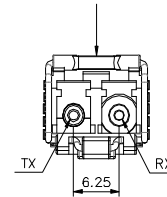
Top View



Front View

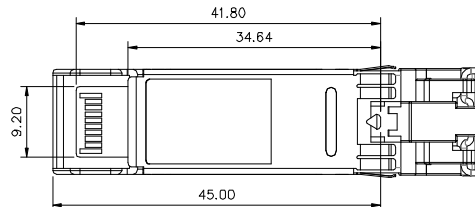


LATCH COLOR  
Blue



Side View

Bottom View



Note: Specifications subject to change without notice.

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**REVISION HISTORY**

Version	Subject	Release Date
1.0	Initial datasheet	2024/2/20
2.0	Add Feature: Rx Output Amplitude Control Implemented	2024/9/19